

Load Check

Read single Reg.	ID Code	Function Code	Addr Highbyte	Addr Lowbyte	Data Highbyte	Data Lowbyte	CRC Lowbyte	CRC Highbyte
	01	03 or 04	00	xx	00	01	xx	xx
Feedback	ID Code	Function Code	Byte Count	Data Highbyte	Data Lowbyte	CRC Lowbyte	CRC Highbyte	
	01	03 or 04	02	xx	xx	xx	xx	
Write single Reg.	ID Code	Function Code	Addr Highbyte	Addr Lowbyte	Data Highbyte	Data Lowbyte	CRC Lowbyte	CRC Highbyte
	01	06	00	xx	00	xx	xx	xx
Feedback	ID Code	Function Code	Addr Highbyte	Addr Lowbyte	Data Highbyte	Data Lowbyte	CRC Lowbyte	CRC Highbyte
	01	06	xx	xx	xx	xx	xx	xx

LD-A%(Unit 1A) MODBUS表

請注意Data Address為10進位制

Data Address(2bytes)	LED Display	Data Description(2bytes)	EEPROM Addr	Default	Limit
0000 (Read only)	tic / xxmA or xxxv	TIC Input Value (Unit 0.1mA or 0.1V)		0	0~20/4~20mA:0.0~20.0mA, 0~5/1~5V:0.0~5.0v, 0~10/2~10V:0.0~10.0v
0001 (Read only)	ct / xxxA	CT Current (Unit 0.1A)		0	0.0~999.0A
0002 (Read only)	ct r / xxxA	CT R Current (Unit 0.1A)		0	0.0~999.0A
0003 (Read only)	ct S / xxxA	CT S Current (Unit 0.1A)		0	0.0~999.0A
0004 (Read only)	ct t / xxxA	CT T Current (Unit 0.1A)		0	0.0~999.0A
0005 (Read/Write)	tic / 0-20 or 4-20 or 0-5 or 1-5 or 0-10 or 2-10 or SSR	TIC Input 0~20mA / 4~20mA / 0~5V / 1~5V / 0~10V / 2~10V/SSR	0x14	4~20mA	0 : 0~20mA 1 : 4~20mA 2 : 0~5V 3 : 1~5V 4 : 0~10V 5 : 2~10V 6 : SSR
0006 (Read/Write)	i S.t./1~1000	CT I max set 1~1000 (Unit 1 A)	0x15-0x16	=ctSt.	1~1000
0007 (Read/Write)	St.UP / 0~60	Alarm Delay 0~60sec (Unit 1 sec)	0x17	5	0 : Fastest 60 : Slowest
0008 (Read/Write)	H.Ltd / 0~xxxA	High Limit (Unit 0.1A)	0x18-0x19	20%	0.0~999.0A
0009 (Read/Write)	L.Ltd / 0~xxxA	Low Limit (Unit 0.1A)	0x1A-0x1B	20%	0.0~999.0A
0010 (Read/Write)	bALn / 5~80	Load Un-Balance Value 5%~80% (Unit 1 %)	0x1C-0x1D	10%	5%~80%
0011 (Read/Write)	H. St./ALAm or nULL	High Limit Enable(1 or 0)	0x1E	Alarm	0 : Null 1 : Alarm
0012 (Read/Write)	L. St./ALAm or nULL	Low Limit Enable(1 or 0)	0x1F	Alarm	0 : Null 1 : Alarm
0013 (Read/Write)	bL.St./ALAm or nULL	Un-Balance Enable(1 or 0)	0x20	Alarm	0 : Null 1 : Alarm
0014 (Read/Write)	ct r / ALAm or nULL	CT R Alarm Enable(1 or 0)	0x21	Alarm	0 : Null 1 : Alarm
0015 (Read/Write)	ct S / ALAm or nULL	CT S Alarm Enable(1 or 0)	0x22	Alarm	0 : Null 1 : Alarm
0016 (Read/Write)	ct t / ALAm or nULL	CT T Alarm Enable(1 or 0)	0x23	Alarm	0 : Null 1 : Alarm
0017 (Read/Write)	cmd / tic or m.bUS	Input Control Command(1 or 0)	0x24	TIC	0 : TIC 1: ModBus
0018 (Read/Write)	m.bUS / 0~1023	Modbus Input Value (10bits)		0	0~1023
0019 (Read/Write)	m.StP. / 0 or LAsT	Modbus Stop Control Command(1 or 0)	0x25	0	0 : 0 1: Last
0020 (Read/Write)	bAUd / 9600 or 1920	Baud Rate 9600 / 19200 bps(1 or 0)	0x26	9600	0 : 9600bps 1 : 19200bps
0021 (Read/Write)	dAtA / 8n1 or 8n2 or 8E1 or 8o1	Communication mode N-8-1/N-8-2/E-8-1/O-8-1	0x27	N-8-1	0 : N-8-1 1 : N-8-2 2 : E-8-1 3 : O-8-1
0022 (Read/Write)	id / 1~247	ID Code (1~247)	0x28	1	1~247
0023 (Read/Write)	t.oUt / 1~30	Timeout (1~30)Secs	0x29	30	1~30
0024 (Read only)		Warning Message		0	Bit0:CT R HighAlarm Bit1:CT R LowAlarm Bit2:CT S HighAlarm Bit3:CT S LowAlarm Bit4:CT T HighAlarm Bit5:CT T LowAlarm Bit6:CT R UnBalance Bit7:CT S UnBalance Bit8:CT T UnBalance Bit9:CT R Over Bit10:CT S Over Bit11:CT T Over

請注意Data Address為10進位制 例: 10進位制 0010 = 16進位制 000A , 16進位制 0010 = 10進位制 0016

Ex Read Output Max Reg. : 01 04 00 02 00 01 90 0A

Ex Read Output Voltage Reg. : 01 04 00 00 00 01 31 CA